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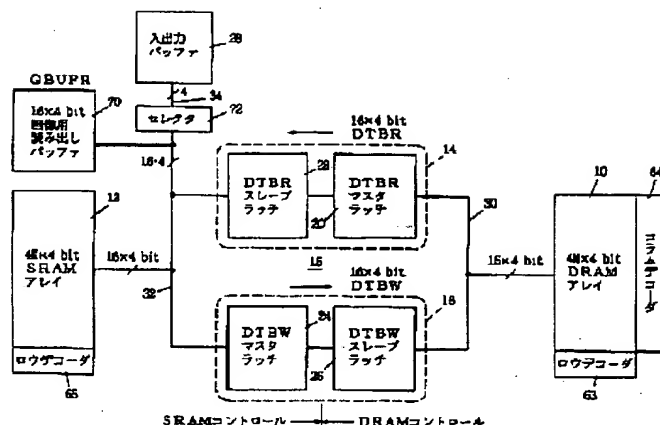PUBLICATION DATE : 07-02-97

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TITLE : SEMICONDUCTOR MEMORY DEVICE

ABSTRACT : PROBLEM TO BE SOLVED: To provide a semiconductor memory device for the use of multi-media with which efficient cache for image data and CUP cache with a high hit rate can be provided.

SOLUTION: This semiconductor memory device is provided with a DRAM (dynamic RAM) array 10, SRAM (static RAM) array 12, DTBW 16 (write data transfer buffer) for transferring data from the SRAM array to the DRAM array, and DTBR (read data transfer buffer) 14 for transferring data from the SRAM array to the DRAM array. These DTBR and DTBW can be accessed through an input / output buffer 28. This semiconductor memory device is further provided with a read buffer 70 for image for storing only data for image. This read buffer 70 for image outputs its stored data through the input / output buffer 28 to the device outside, receives and stores image data from the DRAM array through the DTBR 14. The cache for image data in the optimum size can be provided.

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